

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5	Alan ADJ1 Grebene	USPAT	OR	OFF	2004/11/15 13:52
L2	34	"5,043,731" "4,713,649" "4,703,302"	USPAT	OR	OFF	2004/11/15 13:55
L3	0	successive approximation converter sample hold switched capacitor comparator latches switch	USPAT	SAME	OFF	2004/11/15 13:59
L4	6	successive approximation sample hold switched capacitor comparator	USPAT	SAME	OFF	2004/11/15 14:19
L5	5	successive approximation sample hold switched capacitors comparator	USPAT	SAME	OFF	2004/11/15 15:19
L6	13	successive approximation sample hold switched comparator	USPAT	SAME	OFF	2004/11/15 15:24
L7	6	successive approximation sample hold switched capacitor comparator	USPAT; EPO; JPO; DERWENT	SAME	OFF	2004/11/15 14:16
L8	1	successive approximation sample hold switched comparator	EPO; JPO; DERWENT	SAME	OFF	2004/11/15 14:18
L9	2	sar sample hold switched capacitor comparator	USPAT	SAME	OFF	2004/11/15 14:23
L10	3538	341/172 341/150 341/163 341/150 341/143	USPAT	OR	OFF	2004/11/15 14:48
L11	43	sar set reset	USPAT	WITH	OFF	2004/11/15 14:48
L12	2	successive approximation sample hold switched comparator set	USPAT	SAME	OFF	2004/11/15 15:17
L13	1	sar sample hold switched capacitors comparator	USPAT	SAME	OFF	2004/11/15 15:20
L14	0	sar sample hold switched capacitors comparator AND reset	USPAT	SAME	OFF	2004/11/15 15:21
L15	1	sar sample hold switched comparator AND reset	USPAT	SAME	OFF	2004/11/15 15:21
L16	6	successive approximation sample hold switched comparator AND reset	USPAT	SAME	OFF	2004/11/15 15:22
L17	1	successive approximation sample hold switched comparator	EPO; JPO; DERWENT	SAME	OFF	2004/11/15 15:24

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	180	sar set reset	USPAT	SAME	OFF	2004/11/16 07:45
L2	9	sar set reset AND 341/161	USPAT	SAME	OFF	2004/11/16 07:48
L3	4	logic sar set reset AND 341/161	USPAT	SAME	OFF	2004/11/16 08:05
L4	0	logic latches sar set reset AND 341/161	USPAT	SAME	OFF	2004/11/16 07:50
L5	0	logic latch sar set reset AND 341/161	USPAT	SAME	OFF	2004/11/16 07:51
L6	0	logic nand sar set reset AND 341/161	USPAT	SAME	OFF	2004/11/16 07:51
L7	0	logic nor sar set reset AND 341/161	USPAT	SAME	OFF	2004/11/16 07:51
L8	0	nor sar set reset AND 341/161	USPAT	SAME	OFF	2004/11/16 07:51
L9	0	nor sar set reset AND 341/161	USPAT; EPO; JPO; DERWENT	SAME	OFF	2004/11/16 07:51
L10	39	nand sar set reset	USPAT; EPO; JPO; DERWENT	SAME	OFF	2004/11/16 07:52
L11	37	nor sar set reset	USPAT; EPO; JPO; DERWENT	SAME	OFF	2004/11/16 07:53
L12	34	nor nand sar set reset	USPAT; EPO; JPO; DERWENT	SAME	OFF	2004/11/16 07:54
L13	0	nor nand sar set reset AND 341/161	USPAT; EPO; JPO; DERWENT	SAME	OFF	2004/11/16 07:55
L14	0	nand sar set reset AND 341/161	USPAT; EPO; JPO; DERWENT	SAME	OFF	2004/11/16 07:55
L15	9	sar set reset AND 341/161	USPAT; EPO; JPO; DERWENT	SAME	OFF	2004/11/16 07:55
L16	2	nor set reset AND 341/161	USPAT	SAME	OFF	2004/11/16 08:06
L17	13	gates set reset AND 341/161	USPAT	SAME	OFF	2004/11/16 08:06